

# iNEMI Process For Supply Chain Communication

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JIC 10  
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# Agenda

- Introduction
- Methodology
- Roadmap Overview
- Business Issues
- Technology Issues and Needs
- Gap Analysis/Planning
- Project Development/Output
- Summary & Conclusions

# Who Are We?

- iNEMI organization:
  - Corporate membership
  - Not-for-profit, R&D consortium
  - Collaboration defined by organization by-laws, intellectual property policy, and project agreements.
- Member companies/organizations:
  - Leadership OEM, EMS, and Supplier companies
  - Government labs
  - Academic Institutions.
- Small staff provides services to facilitate global collaboration (USA, Asia & Europe):
  - Support to help organize & manage projects
  - Communication services for collaboration
  - Manage Relationships with other Organizations.

# Global Operations

- iNEMI is headquartered in Herndon, Virginia, USA.
- Started iNEMI China Collaboration in 2003.
- Opened an office in Shanghai and added a team member in Europe in 2007.
- Dr. Haley Fu is leading operations in Asia, based in Shanghai, China.
- Grace O'Malley is representing iNEMI in Europe from her base in Ireland.



# OEM/EMS Members



**Agilent Technologies**



**Alcatel-Lucent**

**Boston  
Scientific**

*Delivering what's next.™*



**lenovo**



**Celestica™**

**DELPHI**

**FOXCONN®**



**i n v e n t**



**HUAWEI**



**FLEXTRONICS**

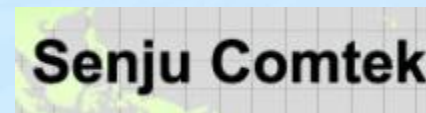


**MICRO SYSTEMS  
ENGINEERING**

**PLEXUS®**



# Supplier Members



# Association/Consortium, Government, Consultant & University Members



# Deliverables

*“Advancing Manufacturing Technology”*

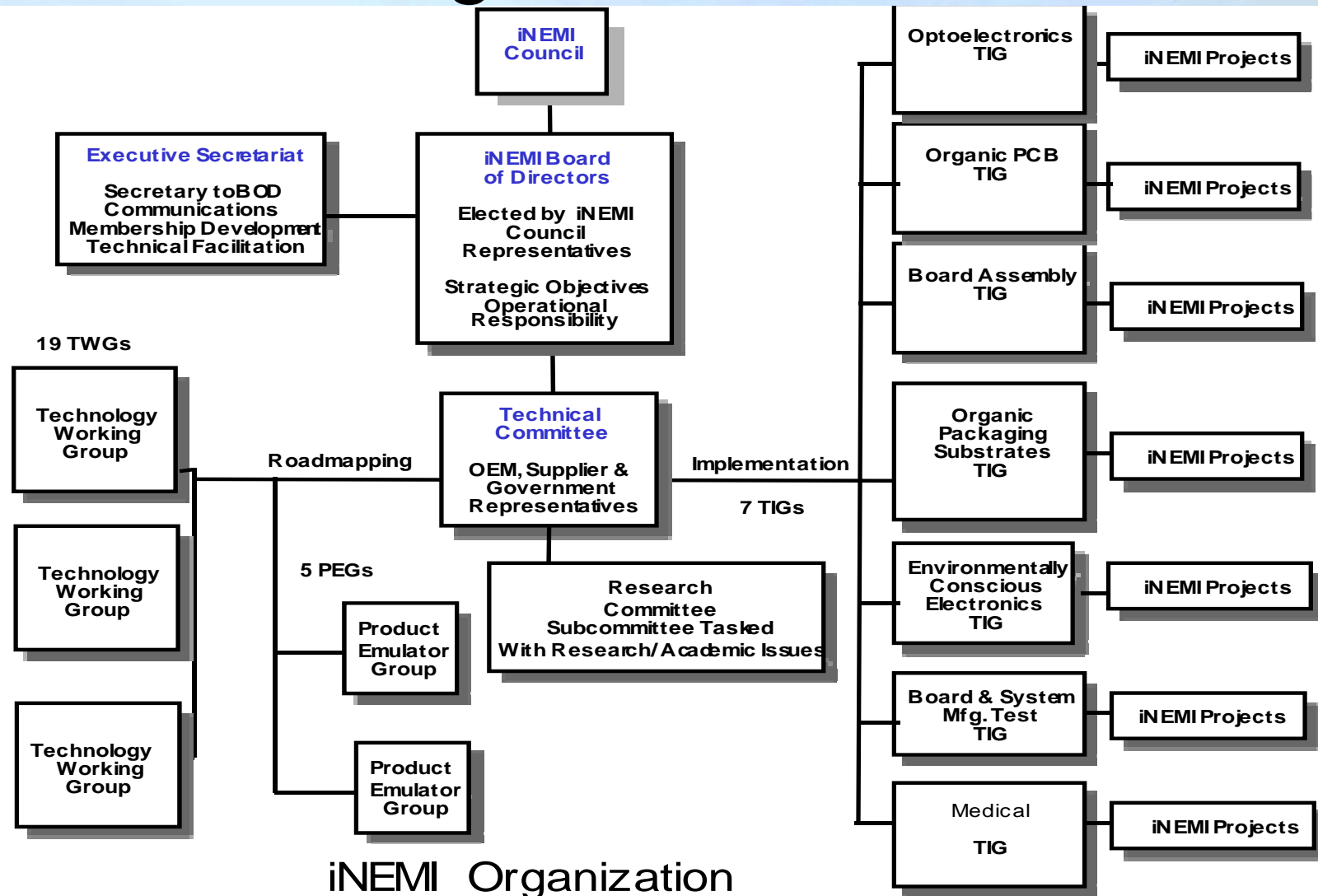
**iNEMI provides five important deliverables:**

- 1. Technology roadmaps**
- 2. Technology deployment projects**
- 3. Research priorities**
- 4. Forums on key industry issues**
- 5. Position papers to focus industry direction**

# Leadership through Innovation

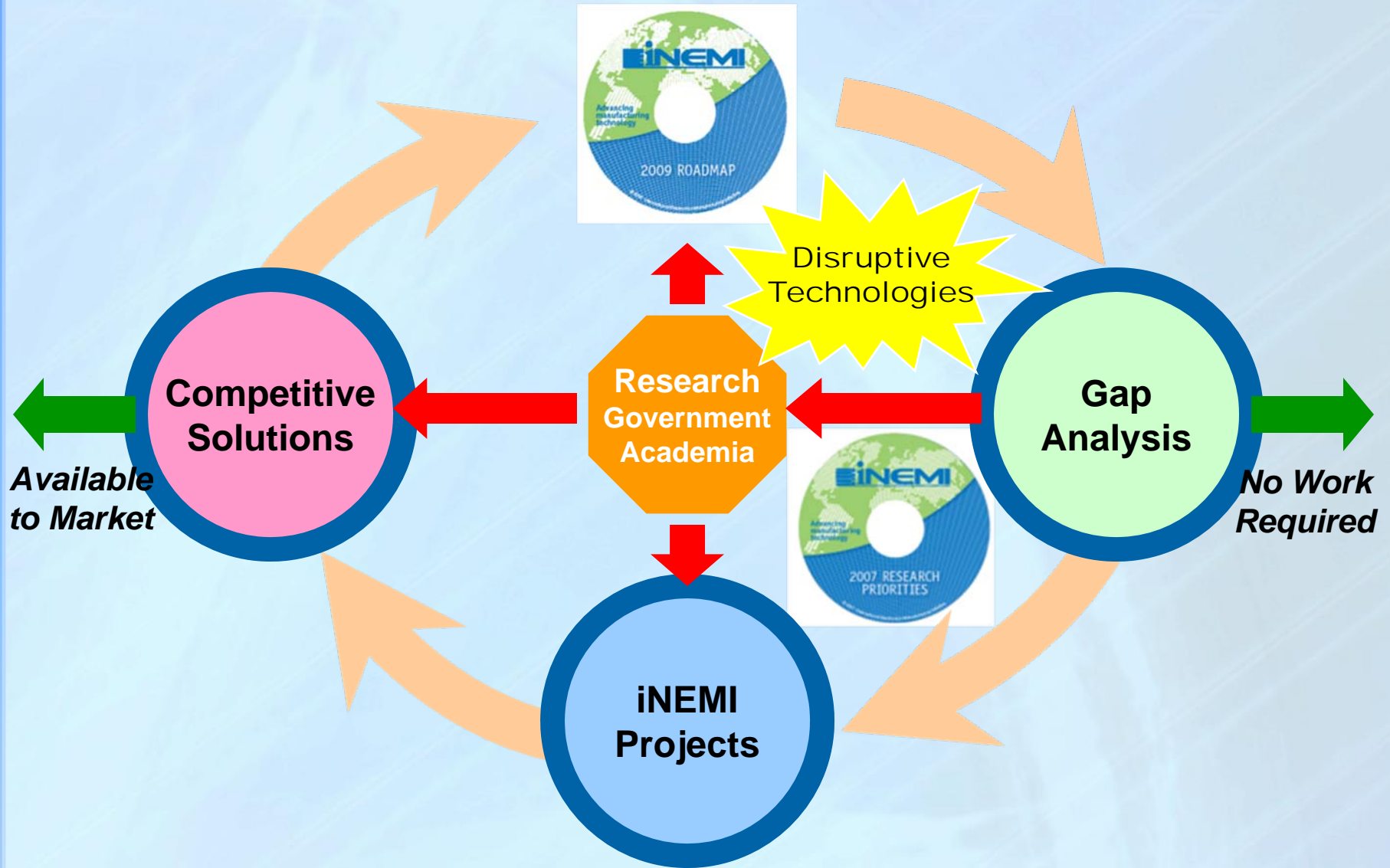
- A proven approach for identifying the technology needs and gaps of the industry through our roadmapping process
- A strong track record of developing supply chains to introduce new materials, processes, and technologies into production
- A research vision with three major thrusts:
  - Energy & the environment
  - Miniaturization
  - Medical electronics

# iNEMI Organizational Chart



iNEMI Organization

# iNEMI Methodology



# 2009 iNEMI Roadmap

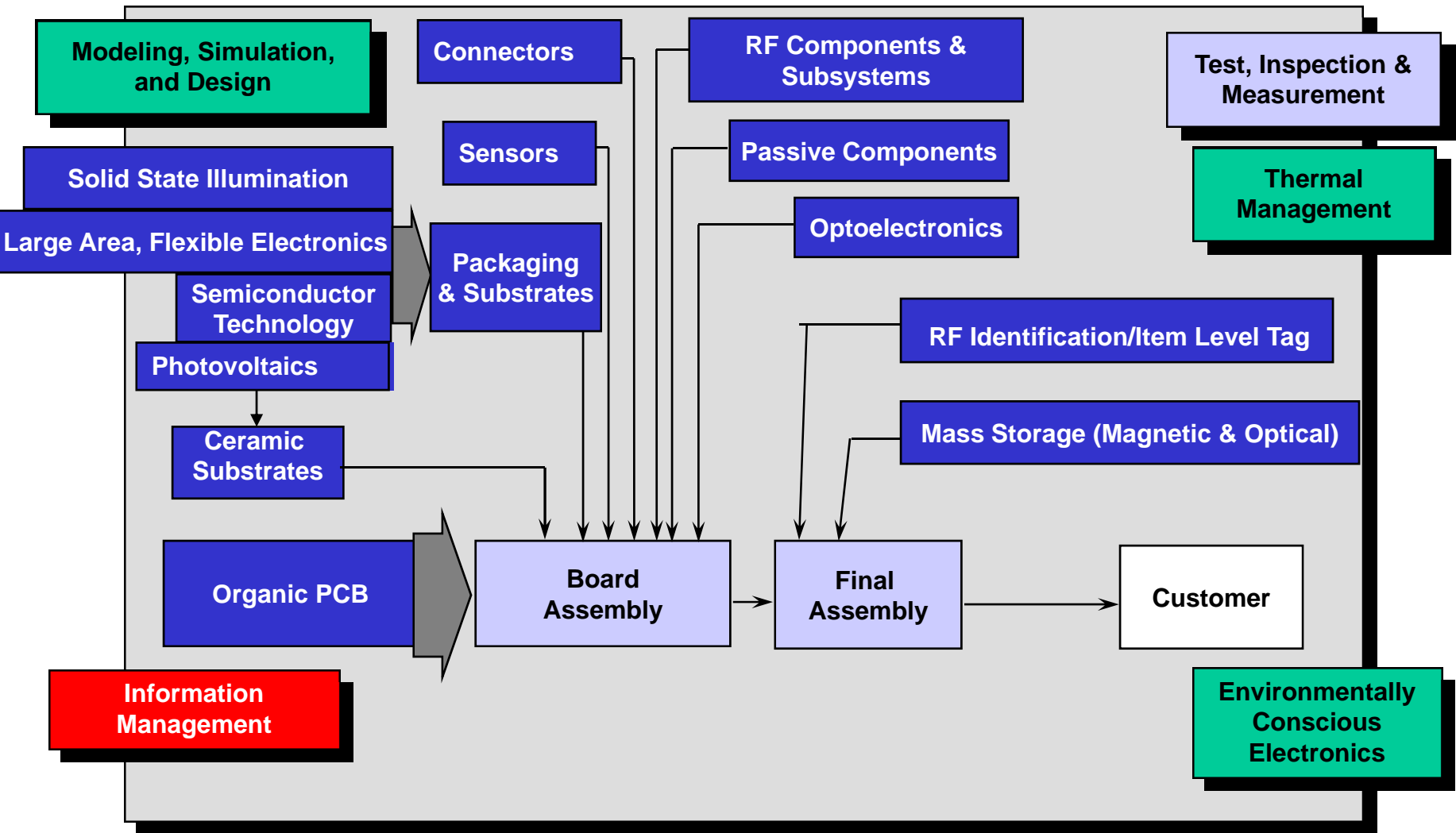


10<sup>th</sup> Jisso International Council Meeting – 18-20 May 2009 – Grenoble France

# Statistics for the 2009 Roadmap

- > 550 participants
- > 250 companies/organizations
- 18 countries from 4 continents
- 20 Technology Working Groups (TWGs)
  - New roadmaps on Solid State Illumination, Photovoltaics and RFID Item-Level Tag
- 5 Product Emulator Groups (PEGs)
- > 1400 pages of information
- Roadmaps the needs for 2009-2019

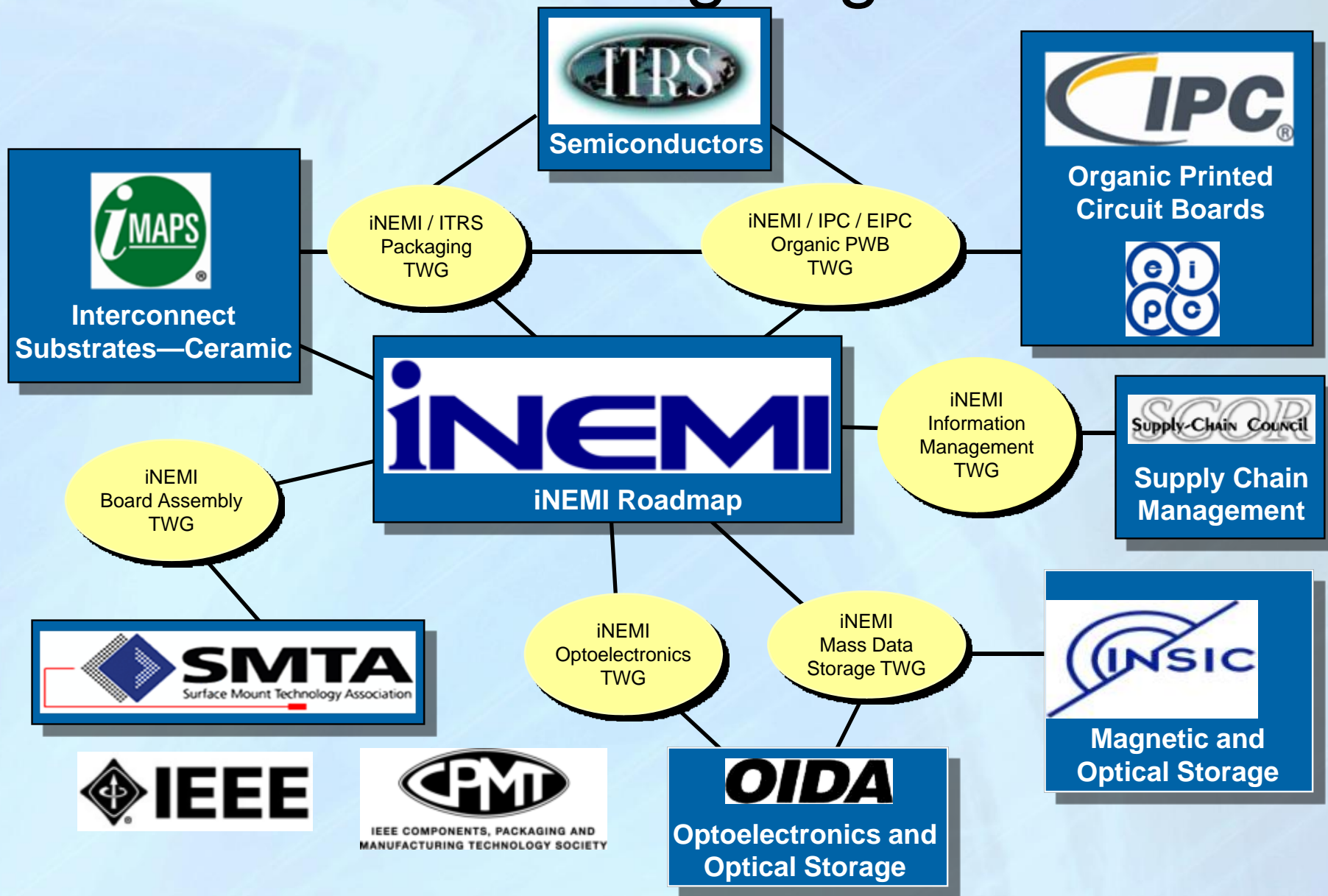
# 2009 Technology Working Groups (TWGs)



Red=Business Green=Engineering Blue=Manufacturing Blue=Component & Subsystem

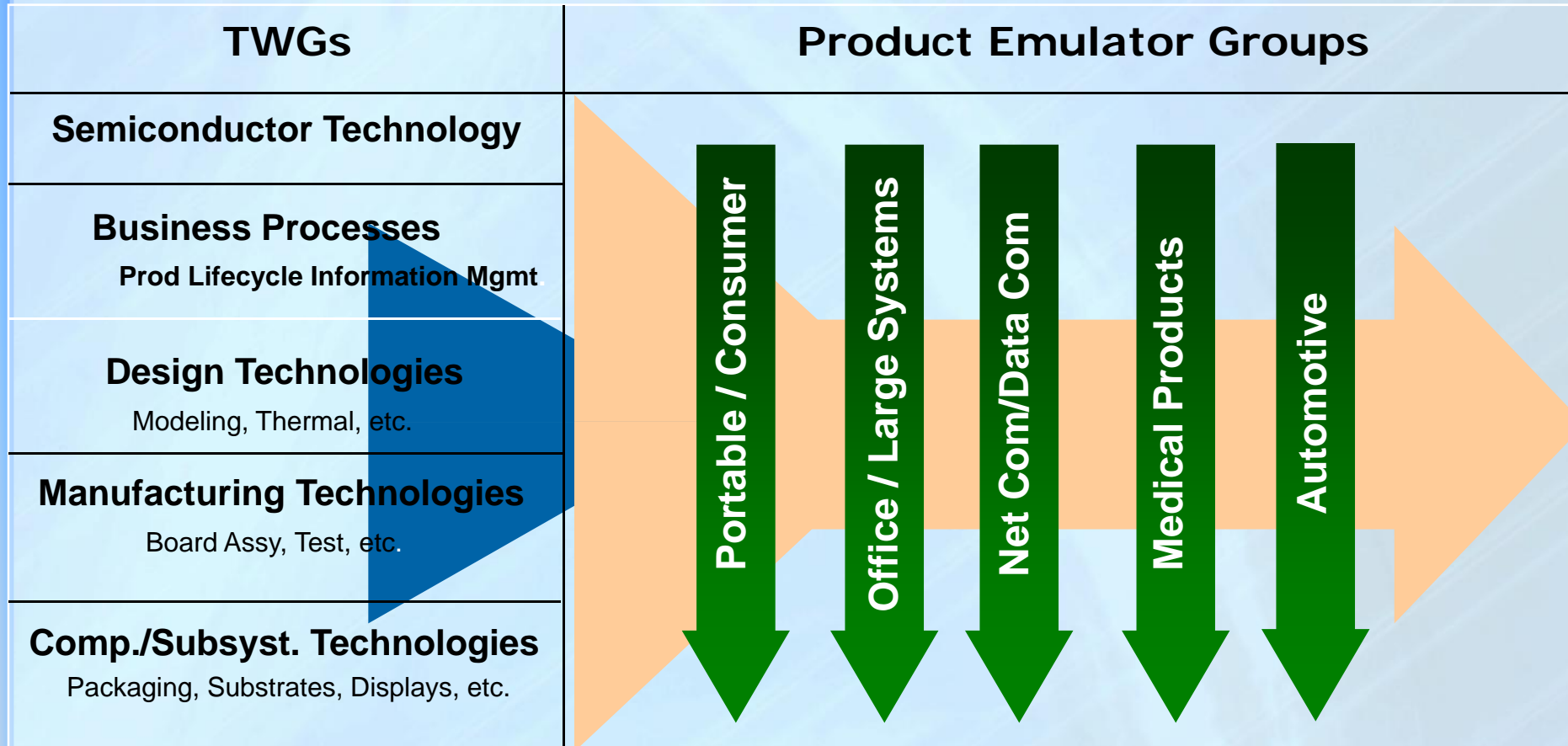


# Nine Contributing Organizations



# Roadmap Development

## *Product Sector Needs Vs. Technology Evolution*



# 2009 Product Emulator Groups

<b>Product Emulator</b>	<b>Chair(s) 2009</b>
<b>Automotive Products</b>	<b>Jim Spall</b>
<b>Medical Products</b>	<b>Anthony Primavera, MSEI Bill Burdick, GE Research</b>
<b>Consumer / Portable Products</b>	<b>Susan Noe, 3M</b>
<b>Office/Large Business System Products</b>	<b>David Lober, Intel David Copeland, Sun</b>
<b>Network, Data, Telecom</b>	<b>John Duffy, Cisco</b>

# 2009 TWG Leadership

<b>Business Processes / Technologies</b>	<b>Chair(s)</b>	<b>Co-Chair(s)</b>
<b>Information Management</b>	<b>Eric Simmon, NIST</b>	<b>Jeff Pettinato, Intel</b>
<b>Design Technologies</b>		
<b>Modeling, Simulation &amp; Design Tools</b>	<b>Yishao Lai, ASE</b>	<b>S.B. Park, Binghamton U.</b>
<b>Environmentally Conscious Electronics</b>	<b>Bob Pfahl, iNEMI</b>	
<b>Thermal Management</b>	<b>Ravi Prasher, Intel</b>	<b>Azmat Malik, Consultant</b>
<b>Manufacturing Technologies</b>		
<b>Final Assembly</b>	<b>John Allen, Celestica</b>	<b>Reijo Tuokko, Tampere U.</b>
<b>Board Assembly</b>	<b>Dongkai Shangguan, Flextronics</b>	<b>Aaron Unterborn, Flextronics Ravi Bhatkal, Cookson</b>
<b>Test, Inspection &amp; Measurement</b>	<b>Mike Reagin, Delphi</b>	<b>Michael J. Smith, Teradyne</b>

# 2009 TWG Leadership (cont.)

<b>Component / Subsystem Technologies</b>	<b>Chair(s)</b>	<b>Co-Chair(s)</b>
<b>Semiconductor Technology</b>	<b>Paolo Gargini, Intel</b>	<b>Alan K. Allan, Intel</b>
<b>Optoelectronics</b>	<b>Dick Otte, Promex</b>	<b>William Ring, WSR</b>
<b>Photovoltaics</b>	<b>Alain Harrus, Cross Link Capital</b>	<b>Jim Handy, Objective Analysis</b>
<b>Packaging</b>	<b>Bill Bottoms, NanoNexus William Chen, ASE</b>	
<b>Passive Components</b>	<b>Philip Lessner, Kemet</b>	<b>John Galvagni, AVX</b>
<b>Connectors</b>	<b>John MacWilliams, Consultant</b>	
<b>RF Components</b>	<b>Ken Harvey, Teradyne</b>	<b>Eric Strid, Cascade MicroTech</b>
<b>Large Area, Flexible Electronics</b>	<b>Dan Gamota, Motorola</b>	<b>Jan Obrzut, NIST Jie Zhang, Motorola</b>
<b>Interconnect Substrates (Ceramic)</b>	<b>Howard Imhof, Metalor</b>	<b>Ton Schless, Sibco</b>
<b>Interconnect PCB (Organic)</b>	<b>John T. Fisher, IPC</b>	<b>Henry Utsunomiya, Consultant</b>
<b>Mass Data Storage</b>	<b>Roger F. Hoyt, Consultant</b>	<b>Tom Coughlin, Coughlin Associates</b>
<b>Solid State Illumination</b>	<b>Marc Chason, Consultant</b>	

# Business Issues



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# Situation Analysis

- Growth of Automotive Electronics
- Convergence (Driven by wireless/portable products)
  - Medical-Consumer
  - Automotive-Entertainment
  - Communication-Entertainment
- Medical Electronics focus shifting towards diagnostics/prevention vs. therapy.
  - Motivations: reduce cost & improve outcomes
  - High volume consumer oriented
  - Challenge for getting quick regulatory acceptance

# Situation Analysis

- Miniaturization and Thinner
- Quality, reliability, cost
- Counterfeit Products
- Time to market
- Increasing Material Restrictions
- Increased focus on Energy Reduction
  - Both product & manufacturing
  - Life-cycle approach

# Strategic Infrastructural Changes

- The restructuring of the electronics industry over the last decade from vertically integrated OEMs to a multi-firm supply chain has resulted in a disparity in R&D needs versus available resources.
- Restructuring has created skill gaps at various nodes of supply chain.
- Critical needs for research and development exist in the middle part of the supply chain (IC assembly services, passive components and EMS assembly) and yet these are the firms least capable of providing the resources.
- A partial solution has been the development of vertical teams to develop critical new technology while sharing the costs.

# Technology Issues and Needs



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# Paradigm Shifts

- Touch Screens becoming main stream.
- MEMs oscillators replacing quartz crystals.
- Emergence of photovoltaics.
- Energy Efficient Lighting.
- Printed electronics.
- Flash memory instead hard drives **for lower power.**
- Wafer level packaging is coming of age.
- ODMs for Cell Phones:
  - Especially for low cost models
  - Can also be used to level load OEM factories
- Migration of where and how passive devices are used.

# Roadmap Conclusions

- Consumer electronics has become the major driving force for our industry:
  - New technology to enable miniaturization
  - Relentless cost reduction
  - Volume manufacturing capability
- Packaging is Key Enabler providing higher density & smaller size:
  - More than Moore
  - 3D configurations, Improved performance
- New global environmental requirements continue to multiply – faster than industry can effectively respond
  - iNEMI and its members plan to play a significant role in preparing industry for these future needs.
- Sustainability will be a major undertaking for industry as well as society.
- Electronic solutions can help to empower people to live a more sustainable lifestyle.

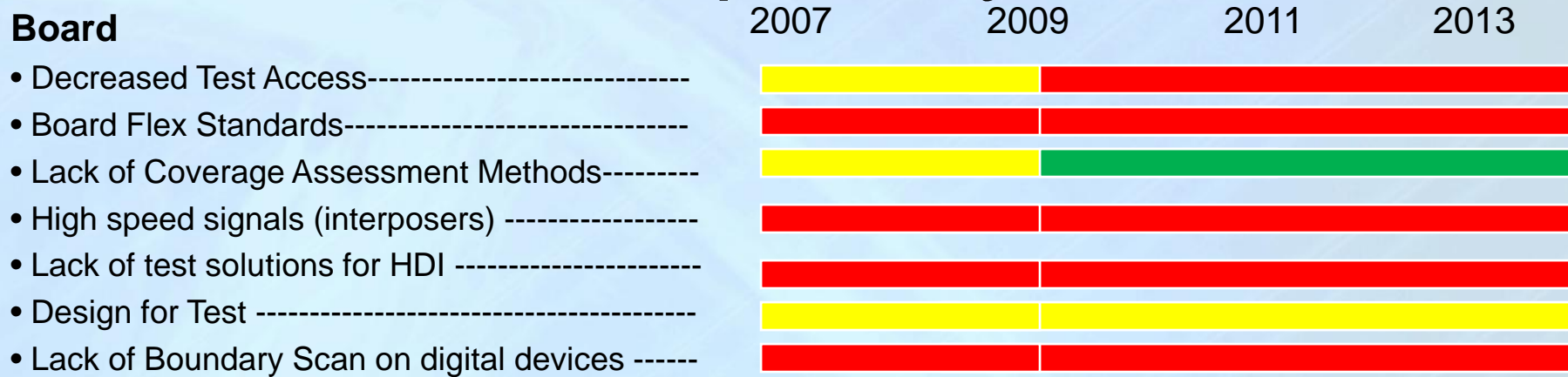
# Gap Analysis And Planning



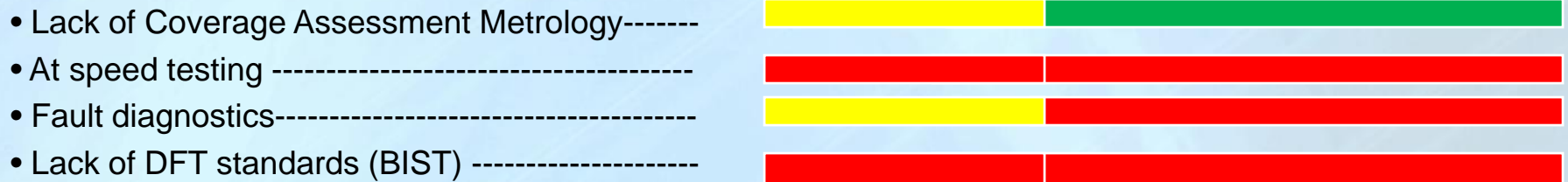
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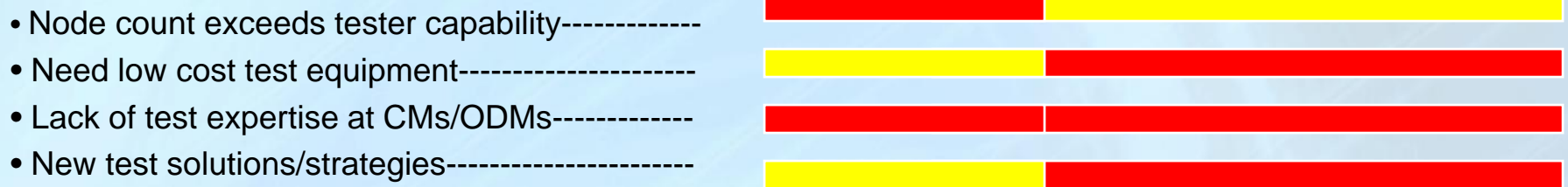
# Test Gap Analysis



## Functional, System



## Test equipment/Tools/Capabilities



Green = No Gap Issues or Resolved  
 Yellow = Known Gap Mitigation Techniques  
 Red = No Known Solution – Development Required

# Test 5 year plan

## Drivers

- Limited board test access
- Cost reductions
- Process optimization
- Test time reduction
- Outsourcing
- Environmental Requirements
- Time to Market

## Attributes

Min Test Pad Size (mils) - 20  
 Via / Pad Size (mils) – 24/10  
 BGA pitch – .4mm /.7mm  
 LF Substrate Materials  
 LF Board Finishes  
 Board node count – <15k  
 New LF solder alloys –  
 I/O Signal speeds – 10Ghz  
 High Density Interconnect (HDI)  
 Fault Coverage  
 Evolving fault spectrum  
 Bonding and Underfill of BGAs

## Deployed Technology

Repeatable LF processes  
 Bead Probe  
 Adv. ICT  
 Adv. Boundary Scan & BIST

## Research /Development

Bead Probe  
 Board Flex impact due to Pb-Free  
 Board Flex standard  
 Design for Test  
 Fault Coverage Metrology

2007

## Attributes

Min Test Pad Size (mils) - 20  
 Probeable Micro Via / Pad Size (mils) – 11/5 (HDI)  
 BGA pitch – .4mm /.6mm  
 LF Substrate Materials  
 LF Board Finishes  
 Nodes – >15k  
 New LF solder alloys –  
 I/O Signal speeds > 20 Ghz  
 High Density Interconnect  
 Evolving fault spectrum

## Deployed Technology

Bead Probe  
 Adv. Test Solutions  
 Adv. Boundary Scan & BIST  
 Common Diagnostics Model  
 Adv. Functional Test Solutions  
 Fault Coverage Metrology

## Research /Development

Bead Probe  
 Board Flex impact due to Pb-Free  
 Board Flex standard  
 Design for Test  
 Adv. Test Solutions (HDI)

2009

## Attributes

Min Test Pad Size (mils) –18  
 Micro Via / Pad Size (mils) – 12/5 (HDI)  
 BGA pitch – .3mm /.5mm  
 LF Substrate Materials  
 LF Board Finishes  
 Nodes – >15K  
 New LF solder alloys -  
 I/O Signal speeds >30 Ghz  
 High Density Interconnect  
 Evolving fault spectrum

## Deployed Technology

Adv. Test Solutions  
 Adv. Structural Test  
 Adv. BIST  
 Virtual Access  
 Common Diagnostics Model  
 Adv. Functional Test Solutions

## Research /Development

Adv. Test Solutions  
 Design for Test  
 New test techniques

2011

## Attributes

Min Test Pad Size (mils) - 18  
 Micro Via / Pad Size (mils) – 12/5 HDI  
 BGA pitch – .3mm /.5mm  
 LF Substrate Materials  
 LF Board Finishes  
 Nodes – >15K  
 New LF solder alloys -  
 I/O Signal speeds >100 Ghz  
 High Density Interconnect  
 Evolving fault spectrum

## Deployed Technology

Adv. Test Solutions  
 Adv. Structural Test  
 Adv. BIST  
 Virtual Access  
 Common Diagnostics Model  
 Adv. Functional Test Solutions

## Research /Development

Adv. Test Solutions  
 Design for Test  
 New test techniques

2013

# iNEMI Projects – Closing The Gaps



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# Key Elements in Value of iNEMI Projects

- **The companies represented**
  - OEMs who are market leaders in innovative products
  - EMS providers who are global leaders in manufacturing
  - Leading suppliers for manufacturing equipment, materials, components, and software.
- **The specific participants within those companies**
  - Board of Directors
  - Technical Committee
  - Council Members

# How Projects are Defined

- **Hold project formation meeting**
- **Members develop Statement of Requirements**
  - User/business case driven
  - What is needed
  - Desired outcome of project
- **Identify leaders**
- **Members develop Statement of Work**
  - Project plan with identified tasks, check points and end dates
  - Companies expected to participate
  - Results expected and how they will be shared outside project
- **Members sign Project Participation Statement**
  - Formally commit resources as agreed
- **Carry out project, report quarterly**

# iNEMI Projects Close Gaps

- Develop/evaluate processes and/or protocols
- Evaluate alternative technologies
- Identify equipment needs
- Identify/evaluate communication schemes
- Determine component requirements
- Develop standards proposals